

# 1250Mbps SFP Transceiver MXPD-245LD-E



#### **Features:**

- Operating data rate 1250Mbps
- Industry standard small form pluggable (SFP) package
- Duplex LC connector
- Single +3.3V power supply
- Differential LVPECL inputs and outputs
- TTL signal detect indicator
- Hot-pluggable capability

### **Application:**

- Switch to Switch interface
- Switched backplane applications
- Router/Server interface
- Other optical transmission systems

#### **Description:**

MXPD-245LD-E series is transceiver compliant with SFP MSA. This series features high performance at low cost, and provides users a cost effective selection for data rate up to 1250Mbps.

The transmitter section and the receiver section work independently in the transceiver. The receiver section contains an InGaAs PIN photo diode, a transimpedance amplifier and a post amplifier (with working data rate up to 1250Mbps), functionally transmit received optical power to steady electrical data. The transmitter section contains a 1550nm DFB laser diode with back-facet monitor and a laser driver with APC function, transmit input electrical data to steady optical output signal.

MXPD-245LD-E provides users a complete selection for different transmission distance to 70KM. The product is compliant with the SFP MSA such as the LC optical interface receptacle, 3.3V supply voltage, TTL signal detect logic.

# **Specification:**

Operating Information				
Part Number	Input/Output	Signal Detect	Voltage	Temperature
MXPD-245LD-E	AC/AC	TTL	+3.3V	0℃ to70℃

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Absolute Maximum Ratings					
Parameter	Symbol	Min.	Max.	Unit	Note
Storage Temperature	Ts	-40	+85	$^{\circ}\mathbb{C}$	
Operating Temperature	To	0	+70	${\mathbb C}$	
Power Supply Voltage	V <sub>CC</sub>	-0.5	3.7	V	

# MXPD-245LD-E (1550nm DFB and PIN, 70KM)

## **Transmitter Optical, Electrical Characteristics**

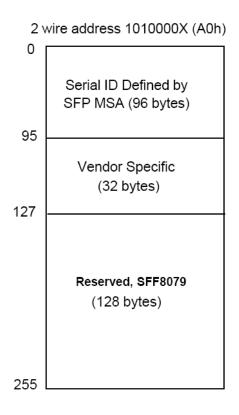
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Input Differential Impedance	R <sub>in</sub>		100		Ω	
Transmitter Differential Input Voltage	V <sub>in</sub> PP	500		1200	mV	
Transmit Disable Input High Voltage		2.0			V	
Transmit Disable Input Low Voltage				0.8	V	
Transmit Fault Output High Voltage		2.0			V	
Transmit Fault Output Low Voltage				0.8	V	
Transmit Disable Assert Time			0.14	5	μs	
Optical Transmit Power	Po	0	2	5	dBm	Average Power
Extinction Ratio	ER	9			dB	
Total Jitter	TJ			0.431	UI	
Side Mode Suppression Ratio	SMSR	30			dB	
Central Wavelength	λc	1530	1550	1570	nm	
Output Spectrum Width (RMS)	Δλ			0.8	nm	

# **Receiver Optical, Electrical Characteristics**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Receiver Differential Output Voltage	V <sub>out</sub> PP	400		1600	mV	
Receiver Loss of Signal Output Voltage -High		2			V	
Receiver Loss of Signal Output Voltage -Low				0.8	V	
Sensitivity	Sen			-24	dBm	BER<1E-12
Maximum Input Power	P <sub>inMAX</sub>	-3			dBm	
Signal Detect Range		-35		-24	dBm	
Signal Detect-Hysteresis		0.5			dB	
Central Wavelength	λc	1260		1600	nm	



## **EEPROM Memory Map:**



## **EEPROM Serial ID Memory Contents:**

Accessing Serial ID Memory uses the 2 wire address 1010000X (A0). Memory Contents of Serial ID are shown in the table below:

# MXPD-245LD-E (1550nm DFB and PIN, 70KM)

Data Address	Size (Bytes)	Name of Field	Contents(Hex)	Description			
	BASE ID FIELDS						
0	1	Identifier	03	SFP			
1	1	Ext. Identifier	04	SFP function is defined by serial ID only			
2	1	Connector	07	LC Connector			
3-10	8	Transceiver	00 00 00 02 12 00 01 01	Transceiver Codes			
11	1	Encoding	01	8B/10B			
12	1	BR, Nominal	0D	1250 Mbps			
13	1	Reserved	00				
14	1	Length(9um,km)	50	Transceiver transmit			
15	1	Length (9um)	00	distance (70KM)			
16	1	Length (50um)	00	Not compliant			

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# **SFP Series**

17	1	Length (62.5um)	00			
18	1	Length (Copper)	00	Not compliant		
19	1	Reserved	00	,		
20-35	16	Vendor name	48 47 20 47 45 4E 55 49 4E 45 00 00 00 00 00 00	"HG GENUINE" (ASCII)		
36	1	Reserved	00			
37-39	3	Vendor OUI	00 00 00			
40-55	16	Vendor PN	4D 58 50 2D 32 34 33 53 2D 32 30 00 00 00 00 00	"MXPD-245LD-E" (ASCII)		
56-59	4	Vendor rev	00 00 00 00			
60-61	2	Wavelength	05 1E	Transceiver wavelength		
62	1	Reserved	00			
63	1	CC_BASE	Check Sum (Variable)	Check code for Base ID Fields		
		EXTENDED II	O FIELDS			
64-65	2	Options	00 1A	TX_DISABLE, TX_FAULT and Loss of Signal implemented.		
66	1	BR, max	00			
67	1	BR, min	00			
68-83	16	Vendor SN	20 20 20 20 20 20 20 20 38 37 36 35 34 33 32 31	Serial Number of transceiver (ASCII). For example "87654321".		
84-91	8	Date code	30 34 31 32 30 35 20 20	Manufactory date code. For example "041205".		
92	1	Diagnostic Monitoring Type	00	DDM Not Implemented		
93	1	Enhanced Options	00	DDM Not Implemented		
94	1	SFF-8472 Compliance	00	DDM Not Implemented		
95	1	CC_EXT	Check Sum (Variable)	Check sum for Extended ID Field.		
	VENDOR SPECIFIC ID FIELDS					
96-127	32	Vendor Specific	Read only	Depends on customer information		
128-255	128	Reserved	Read only	Filled by zero		



# **Pin Definition:**

20	VEET
19	TD-
18	TD+
17	VEET
16	VCCT
15	VCCR
14	VEER
13	RD+
12	RD-
11	VEER

1	VEET
2	TX Fault
3	TX Disable
4	MOD_DEF(2)
5	MOD_DEF(1)
6	MOD_DEF(0)
7	Rate Select
8	LOS
9	VEER
10	VEER

Top of board

Bottom of board

Figure1

# **Pin Assignment:**

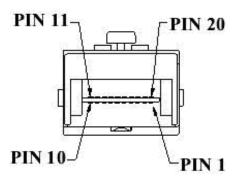


Figure2

Pin	Name	Description
1	VEET	Transmitter Ground
2	TXFAULT	Transmitter Fault.
3	TXDIS	Transmitter Disable.
4	MOD_DEF(2)	SDA Serial Data Signal
5	MOD_DEF(1)	SCL Serial Clock Signal
6	MOD_DEF(0)	Grounded within the module.
7	Rate Select	No connection required
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.
9	VEER	Receiver Ground

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10	VEER	Receiver Ground
11	VEER	Receiver Ground
12	RD-	Receiver Inverted DATA out.
13	RD+	Receiver Non-inverted DATA out.
14	VEER	Receiver Ground
15	VCCR	Receiver Power Supply
16	VCCT	Transmitter Power Supply
17	VEET	Transmitter Ground
18	TD+	Transmitter Non-Inverted DATA in.
19	TD-	Transmitter Inverted DATA in.
20	VEET	Transmitter Ground

# **Block Diagram Of Transceiver**

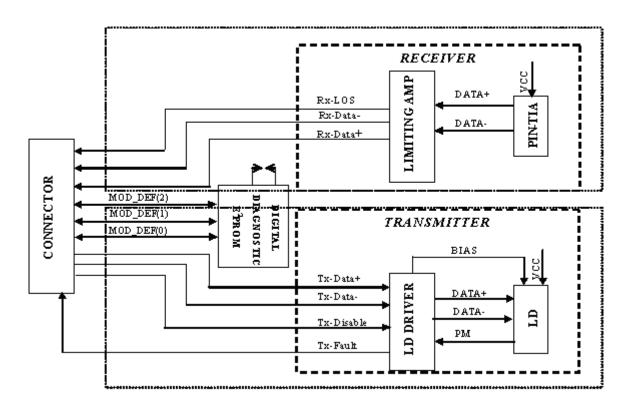


Figure3



#### **Transmitter Section**

#### **TX-FAULT**

TX Fault is an open collector/drain output, which should be pulled up with a  $4.7K-10K\Omega$  resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V. When sensing an improper power level in the laser driver, the SFP sets this signal high and turns off the laser. TX-FAULT can be reset with the TX-DISABLE line. The signal is in TTL level.

#### **TX-DISABLE**

TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a  $4.7-10~\text{K}\Omega$  resistor. Its states are: Low (0-0.8V): Transmitter on; (>0.8, < 2.0V): Undefined; High (2.0-3.465V): Transmitter Disabled; Open: Transmitter Disabled. The TX-DISABLE signal is high (TTL logic "1") to turn off the laser output. The laser will turn on when TX-DISABLE is low (TTL logic "0").

#### **TD-/+**

These are the differential transmitter inputs. They are AC-coupled, differential lines with  $100\Omega$  differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500-2400 mV (250-1200 mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250-600 mV single-ended) be used for best EMI performance.

#### **Receiver Section**

#### **RX-LOS**

LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a  $4.7K-10K\Omega$ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

The RX-LOS is high (TTL logic "1") when there is no incoming light from the companion transceiver. This signal is normally used by the system for the diagnostic purpose. The signal is operated in TTL level.

#### **RD-/+**

These are the differential receiver outputs. They are AC coupled  $100\Omega$  differential lines which should be terminated with  $100~\Omega$  (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 – 1000 mV single ended) when properly terminated.

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#### Mod-Def 0, 1, 2.

Mod-Def 0, 1, 2. These are the module definition pins. They should be pulled up with a 4.7 - 10K resistor on the host board to supply less than VccT+0.3V or VccR+0.3V.

Mod-Def 0 is grounded by the module to indicate that the module is present.

Mod-Def 1 is clock line of two wire serial interface for optional serial ID.

Mod-Def 2 is data line of two wire serial interface for optional serial ID.

#### **Recommended Interface Circuit:**

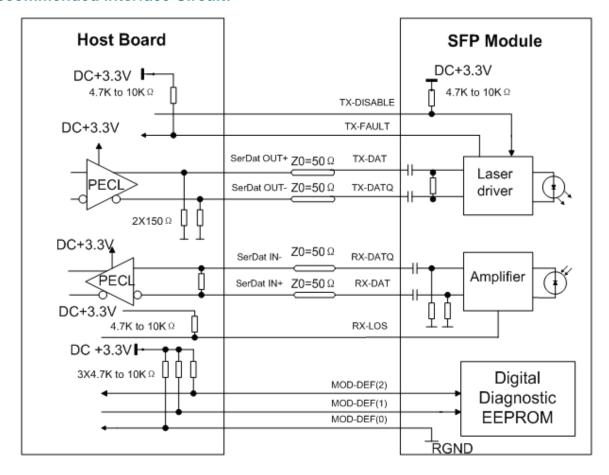


Figure4



## **Dimensions:**

Dimensions are in millimeters. All dimensions are ±0.1mm unless otherwise specified. (Unit: mm).

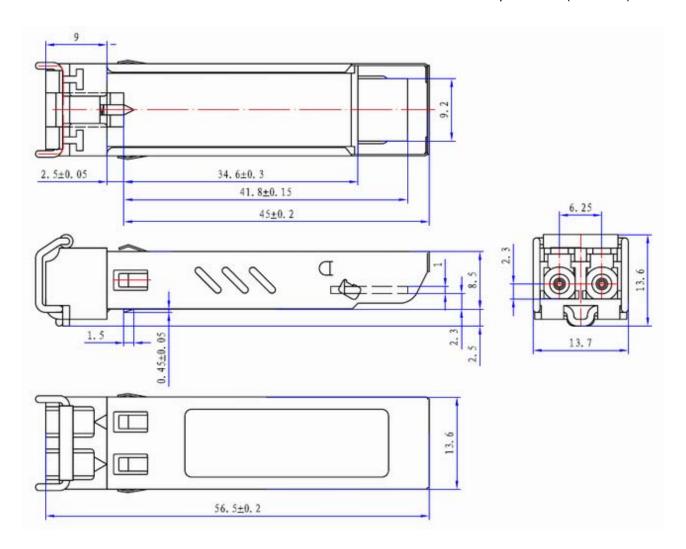
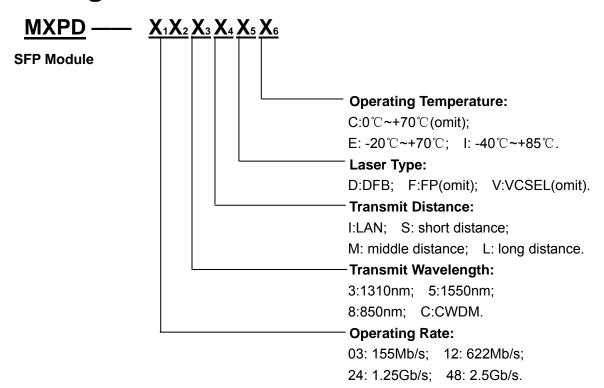


Figure5



## **Ordering Information:**

# **Digital Transceiver Denominate Rule**



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